

RETHINKING AI: ENERGY EFFICIENCY AND THE FUTURE OF COMPUTATION

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ABSTRACT

Over the past decade, neural networks have been a revolution in the computing landscape. Chatbots [1], medicine [2], robotics [3], and communications [4] are just a few examples of how deep learning has outperformed previous state-of-the-art technologies. The success of deep learning has come at a cost: the energy demands of running such models on conventional hardware architectures are becoming unsustainable. Indeed, the training of deep learning models with millions or even billions of parameters requires servers equipped with specialized GPU or TPU producing roughly the same amount of CO₂ emission as the footprint of five cars [5]. Their development in real-world applications, such as autonomous driving, can be even more demanding due to the need for continuous-time inference.

Therefore, as digital computing nears its energy efficiency limits, especially in AI applications, neuromorphic [6] and analog [7] computing are gaining renewed attention as a necessary and promising alternative. Neuromorphic chips aim to replicate how neurons and synapses process

signals in biological neural networks, where the synergy between parallelism, asynchronous signal propagation, and unified memory-computations unit enables the brain to operate with such high energy efficiency. Despite neuromorphic chips can be used to speed up existing AI models, it is essential to design new models and algorithms that can embrace and exploit these characteristics. On the other hand, analog hardware leverages the continuous dynamics of physical phenomena, such as electrical or material properties, to perform computation with significantly lower energy and latency than digital systems, which abstract computations into discrete logic and memory units. This makes it a compelling path toward sustainable AI, particularly for edge devices and real-time inference tasks where power constraints are critical.

Despite their promises, both neuromorphic and analog computing raise key research questions, such as designing algorithms that are robust to the inherent noise of analog substrates, understanding the levels of precision and reliability for different AI tasks, and co-designing models and circuits to balance benefits with scalability and programmability.

This mini-symposium explores brain-inspired and analog computing paradigms that promise transformative advances in energy-efficient AI. We discuss both theoretical foundations and practical applications of neuromorphic and analog computing, highlighting key challenges, recent breakthroughs, and future directions. The event aims to bring together machine learning researchers and hardware engineers to to promote discussion on the design of new generation AI systems for non-traditional and energy-efficient hardware platforms.

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